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[PDF] ► A fully bypassed six-issue integer datapath and **register** file on the itanium-2 microprocessor
ES Fetzer, M Gibson, A Klein, N Calick, C ... - IEEE Journal of Solid-State Circuits, 2002 - sysdoc.door.ch

... To control this network, over 280 **by-pass** comparators are ... manage: the **register** file
and the late **bypass mux**. The **register** file fea- tured wordlines with four ...

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[PDF] ► Energy-efficient **register** access

J Tseng, K Asanovic - Proc. of the 13th Symposium on Integrated Circuits ..., 2000 - Citeseer

... use of all the values in the **by- pass** latches ... provide a separate zero input to the
bypass mux we can ... few registers account for most of the **register** file accesses ...

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Banked multiported **register** files for high-frequency superscalar microprocessors- ► psu.edu (PDF)

JH Tseng, K Asanovic... - ACM SIGARCH Computer Architecture News, 2003 - doi.ieeecomputersociety.org

... a microarchitecture might not support **by- pass** from every ... Wakeup Select Arbitrate

Execute Writeback **Bypass** Read ... This approach adds only a **mux** into the tag ...

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Fast floating point **result** alignment apparatus

AA Bjorksten, DG Mikan Jr, MS Schmookler - US Patent 5,764,549, 1998 - Google Patents

... passed 1bma^ 12A 78 to duce me **Register** 70 simply ... control signal tions through the
B Bypass Mux 53 and ... Also, decoder 110 is **disabled** so that only the leading ...

Cited by 13 - Related articles - All 2 versions

High speed parallel binary multiplier

JW Kronlage - US Patent 4,809,211, 1989 - Google Patents

... There is also a **bypass** line 39 coupled from line 38 ... CKEY on line 37 allows loading
of the Y **register** 36 when ... The output of the Y **multiplexer** 54 for DA and DB ...

Cited by 3 - Related articles - All 3 versions

[PDF] ► Power reduction in VLIW processor with compiler driven **bypass** network

N Goel, A Kumar, PR Panda - VLSID - Citeseer

... number of in- puts to **bypass multiplexer** will increase ... com- pilers hints hardware
for **bypass** control is ... allocation to fully exploit the **by- pass** architecture. ...

Cited by 6 - Related articles - View as HTML - All 10 versions

Boundary-scan **register** cell with **bypass** circuit

NG Jacobson, DR Curd - US Patent 6,314,539, 2001 - Google Patents

... pass data signals output from the shift **register** to the ... from the BSR chain) when
the **bypass MUX** is controlled ... BSR cell also includes mode control **MUX** having a ...

Cited by 11 - Related articles - All 2 versions

Method and apparatus for **disabling** and diagnosing cache memory storage locations

WL Saltz, AS Vezza - US Patent 4,357,656, 1982 - Google Patents

... perfor- mer had the ability to **disable** or "force" ... bit location of the cache control
register 96 are ... an individual output of the index **multiplexer** 46 containing ...

Cited by 23 - Related articles - All 2 versions

... a microinstruction with operands specified by an instruction held in an alias **register**

A Carbine, FS Smith - US Patent 5,222,244, 1993 - Google Patents

... 99 LOGIC MICRO-FLOW II2— WORD **MULTIPLEXER** CONSTANT BUS ... 1 _ ^ 8T J J L **BYPASS**
MULTI- PLEXER ... CONTENTS AVAILABLE TO THE MOUSETRP **MUX**, THROUGHTHE TRANSLATION ...

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A speculative control scheme for an energy-efficient banked **register** file- ► psu.edu (PDF)

JH Tseng, K Asanović - IEEE Transactions on Computers, 2005 - doi.ieeecomputersociety.org

... no optimization techniques are applied to remove or prevent ... slight increase in the overall **register** file area ... in control logic complexity and **bypass mux** size. ...

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[PDF] ► A fully bypassed six-issue integer datapath and **register file** on the itanium-2 microprocessor

ES Fetzer, M Gibson, A Klein, N Calick, C ... - IEEE Journal of Solid-State Circuits, 2002 - sysdoc.door

... control this network, over 280 **by-pass** comparators are ... register must take precedenceover **register file** data as ... are all kept locally in **bypass** control latches ...

Cited by 53 - Related articles - View as HTML - BL Direct - All 27 versions

[A novel renaming scheme to exploit value temporal locality through physical register reuse ...](#) ► psu.edu

[PDF]

S Jourdan, R Ronen, M Bekerman, B Shomar, ... - MICRO-ANNUAL WORKSHOP THEN ANNUAL ... , 1998 -

doi.ieeecomputersociety.org

... scheme introduced in [Mosh97b] proposed to **bypass** the memory ... no writing is performed into the **register file** on a ... This gives the opportunity to remove all the ...

Cited by 100 - Related articles - BL Direct - All 26 versions

[Register packing: Exploiting narrow-width operands for reducing **register file** pressure-](#) ► psu.edu

[PDF] O Ergin, D Balkan, K Ghose, D Ponomarev - Proceedings of the 37th International Symposium on ... , 2004 - doi.ieeecomputersociety.org

... slot address are driven over a shared **bypass** bus, the ... the bus delays permits the write **disable** control to ... needed for a write into the **register file** as well as ...

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[PDF] ► [Energy-efficient register access](#)

J Tseng, K Asanovic - Proc. of the 13th Symposium on Integrated Circuits ... , 2000 - Citeseer

... The base case **register file** always accesses both operands even if the machine ... all instructions make use of all the values in the **by-pass** latches ... 7. **Bypass Skip** ...

Cited by 52 - Related articles - View as HTML - All 32 versions

[The PowerPC 604 risc microprocessor-](#) ► gatech.edu

[PDF] SP Song, M Denman, J Chang - IEEE Micro, 1994 - doi.ieeecomputersociety.org

... **By-pass** logic permits most instructions to complete and write back ... operand to be written to the **register file**, even if ... is a miss, the unit will **bypass** it in ...

Cited by 124 - Related articles - BL Direct - All 7 versions

[PDF] ► [Itanium 2 processor microarchitecture](#)

C McNairy, D Soltis - IEEE Micro, 2003 - doi.polimi.it

... These capabilities remove the burden of ordering and padding instructions ... and eight write ports on the integer **register file** and 20 **bypass** choices support ...

Cited by 123 - Related articles - View as HTML - BL Direct - All 42 versions

[PDF] ► [A 160-mhz, 32-b, 0.5-w CMOS RISC microprocessor](#)

J Montanaro, RT Witek, K Anne, AJ Black, ... - IEEE Journal of Solid-State Circuits, 1996 - Citeseer

... at the end of cycle 3, we **bypass** the **result** ... so for this case, the shifter is **disabled** and bypassed to ... a two-cycle latency for return data to the **register file**. ...

Cited by 543 - Related articles - View as HTML - BL Direct - All 11 versions

[PDF] ► [Dual-core execution: Building a highly scalable single-thread instruction window](#)

H Zhou - Proceedings of the 14th International Conference on ... , 2005 - Citeseer

... keeping the decoded/renamed version can **bypass** some of ... to the front processor's physical **register file**, the renaming ... mode, such invalidation is **disabled** at the ...

Cited by 54 - Related articles - View as HTML - All 21 versions

[Boosting beyond static scheduling in a superscalar processor-](#) ► psu.edu

[PDF]

MD Smith, MS Lam, MA Horowitz - Proceedings of the 17th annual international ..., 1990 - portal.acm.org

... More recent VLIW machines [5] remove this restriction ... allowing loads and **stores** to **bypass** each other ... a reorder buffer [19] associated with each **register file** ...

Cited by 170 - Related articles - All 20 versions

[Banked multiported register files for high-frequency superscalar microprocessors](#) - ► psu.edu (PDF)

JH Tseng, K Asanovic... - ACM SIGARCH Computer Architecture News, 2003 - doi.ieeecomputersociety.org

... a microarchitecture might not support **by-pass** from every ... Wakeup Select Arbitrate

Execute Writeback **Bypass** Read ... Our **register file** structure only allows sharing ...

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ES Fetzer, M Gibson, A Klein, N Calick, C ... - IEEE Journal of Solid-State Circuits, 2002 - sysdoc.door.ch

... To control this network, over 280 **by-pass** comparators are ... match signal before
generating the **mux** selects for a ... are all kept locally in **bypass** control latches ...

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[PDF] ► Energy-efficient register access

J Tseng, K Asanovic - Proc. of the 13th Symposium on Integrated Circuits ... , 2000 - Citeseer

... minimal extra control logic we can also **disable** word lines ... use of all the values
in the **by-pass** latches. ... provide a separate zero input to the **bypass mux** we can ...

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Fast floating point **result** alignment apparatus

AA Bjorksten, DG Mikan Jr, MS Schmookler - US Patent 5,764,549, 1998 - Google Patents

... control signal tions through the B **Bypass Mux** 53 and ... hex alignment circuit 80 is
set, **disabling** thermometer 104 ... Also, decoder 110 is **disabled** so that only the ...

Cited by 13 - Related articles - All 2 versions

Banked multiported register files for high-frequency superscalar microprocessors- ► psu.edu [PDF]

JH Tseng, K Asanovic... - ACM SIGARCH Computer Architecture News, 2003 - doi.ieee.org/10.1109/CSUR.2003.1200000

... a microarchitecture might not support **by-pass** from every ... Wakeup Select Arbitrate

Execute Writeback **Bypass** Read ... This approach adds only a **mux** into the tag ...

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[PDF] ► U-Бда-И"«Бсіл-Й L= 01 [«-Spreg-j-» W-Spreg-|-»| LJ^

GT Б1Д1Д - uhra.herts.ac.uk

... HARP uses register bypassing to remove the potential ... to another pipeline »», Compute
result Figure 4 ... **bypass** source ШшШш **Bypass MUX bypass** sources **Bypass** ...

Related articles

Data memory and method of reading a data memory

HE Oldham - US Patent 5,280,449, 1994 - Google Patents

... efficient embodi- serving as a **bypass multiplexer** is arranged ... output from the data
word selection **multiplexer** 6 have ... 5 and delay line 7 is to **disable** the sense ...

Cited by 8 - Related articles - All 2 versions

Pipelined instruction processor with data bypassing

B Srinivasan, R Sethuraman, CAA Pinto - US Patent App. 10/549,368, 2004 - Google Patents

... unit 30 is enabled when the **disable** information indicates ... 32 should be used as operand,
bypass control unit ... from operand selection unit 34 via **multiplexer** 123. ...

All 4 versions

Combination of forwarding/**bypass** network with history file

BK Flachs, BW Michael - US Patent App. 11/095,908, 2005 - Google Patents

... 5, 2006 COMBINATION OF FORWARDING/BYPASS NETWORK WITH ... and 118 have a **multiplexer**
("MUX") 150 connected on ... **store** data **results**; a multiplexor ("MUX"), coupled to ...

All 2 versions

Digital data processing apparatus having multi-level register file

NS Nunamaker, JC Randolph, K Tsuchiya - US Patent App. 11/835,519, 2007 - Google Patents

... 3 406 Data 305A 34x130 bit 305B 34x130 bit 404 To **By-Pass Mux** op_conflict data ... x4)
-y 3Q6A 128x128 bit 306B 128 x 128 bit To **Result Mux** To **Store** Staging To ...

[All 4 versions](#)

Method and apparatus for **disabling** and diagnosing cache memory storage locations

WL Saltz, AS Vezza - US Patent 4,357,656, 1982 - Google Patents

... **disabled** while allowing the remainder of cache to be Part of the ... perfor- mer had the ability to **disable** or "force ... with the output of the tag **multiplexer** 48 and ...

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Did you mean: bypass + ("register file") + (multiplexer OR **multiplexer** OR MUX) + (disable OR prevent OR stop) + result + store

[PDF] ► A fully bypassed six-issue integer datapath and **register file** on the itanium-2 microprocessor

ES Fetzer, M Gibson, A Klein, N Calick, C ... - IEEE Journal of Solid-State Circuits, 2002 - sysdoc.doors.ch

... To control this network, over 280 **by-pass** comparators are utilized. ... design were

especially difficult to manage: the **register file** and the late **bypass mux**. ...

Cited by 53 - Related articles - View as HTML - BL Direct - All 27 versions

Reducing register ports for higher speed and lower energy- ► psu.edu [PDF]

I Park, MD Powell, TN Vijaykumar - MICRO-ANNUAL WORKSHOP THEN ANNUAL ..., 2002 - doi.ieeecomputersociety.org

... **bypass** inputs (a) Conventional **register file**: Each read port wired to 1 func.

unit. (b) **Bypass hint register file: mux mux Func. Unit mux mux** ...

Cited by 129 - Related articles - BL Direct - All 12 versions

Implementing precise interrupts in pipelined processors- ► umass.edu [PDF]

JE Smith, AR Pleszkun - IEEE Transactions on Computers, 1988 - doi.ieeecomputersociety.org

... in the reorder buffer, then a **multiplexer** is set ... are used prior to being written

into the **register file**. ... an operand designator should generate a **bypass** path to ...

Cited by 458 - Related articles - BL Direct - All 25 versions

[PDF] ► A 160-mhz, 32-b, 0.5-w CMOS RISC microprocessor

J Montanaro, RT Witek, K Anne, AJ Black, ... - IEEE Journal of Solid-State Circuits, 1996 - Citeseer

... at the end of cycle 3, we **bypass** the **result** ... operations comes from a 31-entry **register**

file with three ... tion code computation to swing the PC **multiplexer** for the ...

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[PDF] ► Energy-efficient register access

J Tseng, K Asanovic - Proc. of the 13th Symposium on Integrated Circuits ..., 2000 - Citeseer

... use of all the values in the **by-pass** latches ... provide a separate zero input to the

bypass mux we can ... few registers account for most of the **register file** accesses ...

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[PDF] ► A 600-MHz VLIW DSP

S Agarwala, T Anderson, A H, MD Ales, R ... - IEEE Journal of Solid-State Circuits, 2002 - Citeseer

... to calculate controls for the RF **by-pass** logic and ... reads that are not necessary

due to RF **bypass**. ... from the functional unit and the **register file** is bypassed ...

Cited by 60 - Related articles - View as HTML - BL Direct - All 7 versions

Method and apparatus for delaying writing back the **results** of instructions to a processor

PG Patel - US Patent 5,222,240, 1993 - Google Patents

... destination address, **bypass** path 200 **register file** having a ... **Bypass** path 200 connects the tempo- file comprising at ... register TEMP 148 through **multiplexer** 4 136 ...

Cited by 15 - Related articles - All 2 versions

Register packing: Exploiting narrow-width operands for reducing **register file** pressure- ► psu.edu [PDF]

O Ergin, D Balkan, K Ghose, D Ponomarev - Proceedings of the 37th International Symposium on ..., 2004 - doi.ieeecomputersociety.org

... 2:1 MUX 3:1 MUX 4:1 MUX 4:1 MUX ... assigned slot address are driven over a shared **bypass** bus, the ... of the register, this logic disables the **register file** write in ...

Cited by 44 - Related articles - All 18 versions

[Computer processor having a **register file** with reduced read and/or write port bandwidth](#)

R Yung, N Wilhelm - US Patent 5,761,475, 1998 - Google Patents

... Scoreboard unit 22, and a **register file** 28 positioned ... processor 50 also includes a **bypass** logic circuit 52 ... number of source register **multiplexor (MUX)** circuit 54 ...

Cited by 7 - Related articles - All 4 versions

[A speculative control scheme for an energy-efficient banked **register file**- ► psu.edu \[PDF\]](#)

JH Tseng, K Asanović - IEEE Transactions on Computers, 2005 - doi.ieeecomputersociety.org

... are applied to remove or **prevent** write correlations. ... the slight increase in the overall **register file** area in ... in control logic complexity and **bypass mux** size. ...

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Did you mean to search for: **bypass** + ("register file") + (multiplexer OR **multiplexer** OR MUX) + (disable OR prevent OR stop) + result + store

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